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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/721,225

11/26/2003

Joern Luetzen

INF115

5694

48154

7590

05/12/2006

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EXAMINER

GOUDREAU, GEORGE A

ART UNIT

PAPER NUMBER

1763

DATE MAILED: 05/12/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/721,225	Applicant(s) LUETZEN ET AL.	
	Examiner George A. Goudreau	Art Unit 1763	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 04 January 2006.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) 14-20 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-13 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

George A. Goudreau
GEORGE A. GOUDREAU
PRIMARY EXAMINER
4-06

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

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1. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

2. Claims 1-13 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the enablement requirement. The claim(s) contains subject matter, which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention.

- The specification fails to clearly describe what an area selective etching is.

Further, the specification fails to explain how sections of the semiconductor substrate, which are located below secondary structures, are made available for formation of extended main structures. (See claim 1.);

- The specification fails to explain how an oval structure is first etched into the wafer followed by the etching of a rectangular shaped structure. (i.e.-See claim 7, etc. Please also see the written opinion issued by the German Patent Office regarding this matter.); and

- The specification fails to explain what is meant by the term selection transistors. (i.e.-See claim 13.)

3. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter, which the applicant regards as his invention.

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4. Claims 1-13 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

-The last paragraph of claim 1 is written in a very confusing manner, and should be reworded. (i.e.-The examiner does not understand what is meant by the phrase "...an area selective etching...". Further, the examiner does not understand how "...sections of the semiconductor substrate which are located below secondary structures are made available for formation of extended main structures...");

-In line 3 of claim 4, the usage of the phrase "...at and/ or on the semiconductor wafer." is confusing, and should be reworded.;

-Claim 5 seems to be redundant upon claim 4 upon which it depends.

(i.e.-The examiner cannot discern what applicant is trying to claim in claim 5.);

-In claim 7, the examiner does not understand how the main structure of the semiconductor substrate is provided with an oval cross section.;

-In claim 9, it is not understood how the rectangular surface grid is oriented with a <100> crystal orientation of the monocrystalline silicon.;

-In lines 3-4 of claim 11, the phrase "...the expanding etching process." lacks proper antecedent basis in claim 1 upon which it depends.; and

-In claim 13, the usage of the term "selection transistors" is confusing, and should be reworded. (i.e.-What constitutes being a selection transistor?)

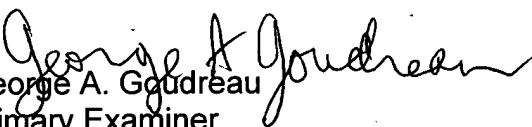
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5. The claims, and the specification are written in such a confusing manner that it is impossible for the examiner to conduct a proper search of applicant's claims.

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

7. Any inquiry concerning this communication should be directed to examiner

George A. Goudreau at telephone number (571)-272-1434.


George A. Goudreau
Primary Examiner
Art Unit 1763